

CRYSTAL CLEAR TECHNOLOGY

Product Specification

T350A22N00

(REVISION1)

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2.0 Records of Revision

| Rev | Date | Item | Page | Comment | Originator | Checked By |
|------|----------|------|------|-----------------|------------|------------|
| REV1 | 05.09.17 | | | Initial Release | SCChong | Azhar |



3.0 General Specification

T350A22N00 is 3.5" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs control circuit, and LED backlight. This display area contains 240 x 320 pixels and can display up to 262k colors. This product compliant with RoHS environmental requirement.

| Item | Contents | Unit | Note |
|----------------------------------|-----------------------------|----------|------|
| LCD Type | 3.5" TFT | - | |
| Display color | 262k | | 1 |
| Viewing Direction (Optimum View) | All | O 'Clock | |
| Module size | 64.16(W) x 83.39(H) x 3.8T) | mm | 2 |
| Active Area(W×H) | 53.28(W) x 71.04(H) | mm | |
| Number of Dots | 240×RGB×320 | dots | |
| Pixel Pitch | 0.074(RGB) x 0.222 | mm | |
| Controller | HX8347A or equivalent | - | |
| Backlight | 7 White LEDs | pcs | |
| Brightness | 400(typ) | cd/m2 | |
| Interface Mode | MCU / RGB | - | |
| Data Transfer | RGB | - | |

Note1: Color tone is slightly changed by temperature and driving voltage.

Note2: FPC or wire are not included.

Note3: Brightness on LCD surface. Module with CTP or RTP, brightness will be about 20% (max) lower on the touch panel surface.

AVAILABLE OPTION

| |
|---|
| <div style="display: flex; justify-content: center; gap: 10px;"> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;"> </div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;">X</div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;">0</div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;">0</div> <div style="border: 1px solid black; width: 20px; height: 20px; display: flex; align-items: center; justify-content: center;">X</div> </div> |
| <p>TOUCH PANEL</p> <p>N : Without Touch Panel</p> <p>C : Capacitive Touch Panel</p> <p>R : Resistive Touch Panel</p> |
| <p>SEMI - CUSTOMISE (MINOR CHANGES FROM STANDARD MODEL)</p> <p>00 - STANDARD SPECIFICATION MODEL</p> |
| <p>OTHER OPTION</p> <p>C : STANDARD COVER LENS</p> |
| <p>REFER TO FACTORY FOR FURTHER INFORMATION.</p> <p>TERMS AND CONDITIONS APPLY</p> |



4.0 Absolute Maximum Ratings

4.1 Electrical Absolute Maximum ratings (Vss = 0V, Ta = 25°C)

| Item | Symbol | Min. | Max. | Unit | Note |
|-------------------------|--------|------|------|--------|------|
| Supply Voltage (Analog) | VCI | -0.3 | 4.0 | V | 1, 2 |
| Input Voltage (I/O) | IOVCC | -0.3 | 4.0 | V | |
| Current of LED | ILED | 0 | 30 | mA/led | |

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged.
2. $V_{DD} > V_{SS}$ must be maintained.
3. Please be sure users are grounded when handing LCD Module.

4.2 Environmental Absolute Maximum Ratings

| Item | Storage | | Operating | | Note |
|---------------------|---------|------|-----------|------|------|
| | MIN. | MAX. | MIN. | MAX. | |
| Ambient Temperature | -30°C | 80°C | -20°C | 70°C | 1,2 |
| Humidity | - | - | - | - | 3 |

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature. The phenomenon is reversible.
3. $T_a \leq 40^\circ\text{C}$ and 85%RH MAX.
($T_a > 40^\circ\text{C}$. Absolute humidity must be lower than the humidity of 85%RH at 40°C)

**5.0 Electrical Characteristics and Instruction Code****5.1 Electrical Characteristics (V_{SS} = 0V, T_a = 25°C)**

| Parameter | Symbol | Rating | | | Unit | Condition |
|---------------------------|-----------------|----------|------|----------|------|-----------|
| | | Min. | Typ. | Max. | | |
| Analog Operating Voltage | V _{CI} | 2.4 | - | 3.3 | V | |
| Logic Operating Voltage | IOVCC | 1.8 | - | 3.3 | V | Note 1 |
| Logic High Input Voltage | V _{IH} | 0.7IOVCC | - | IOVCC | V | |
| Logic Low Input Voltage | V _{IL} | -0.3 | - | 0.3IOVCC | V | |
| Logic High Output Voltage | V _{OH} | 0.8IOVCC | - | - | V | |
| Logic Low Output Voltage | V _{OL} | - | - | 0.2IOVCC | V | |
| Supply Current | I _{DD} | TBD | TBD | TBD | mA | |

Note:

1. Supply the digital IOVCC voltage equal to or less than the analog voltage (V_{CI}).

5.2 LED Backlight Specification (V_{SS} = 0V, T_a = 25°C)

| Item | Symbol | Condition | Min | Typ | Max | Unit | Note |
|----------------|------------------|-----------|-------|-----|-----|------|------|
| Supply Voltage | V _{LED} | - | 5.6 | 6.0 | 6.2 | V | 1 |
| Supply Current | I _f | - | - | 140 | - | mA | 2 |
| Lifetime | | | 50000 | - | - | | 3 |

Note:

1. V_{LED}=V_{LED} (+)-V_{LED} (-).
2. It is recommended that customer supply constant current to prolong the led lifetime and optimum led performance
3. Definition of Lifetime: Luminance < 50% of initial Luminance
(Test condition: T_a = 25°C, Constant current supply (typical Value))

**5.3 Interface Signal**

| Pin No. | Symbol | Function |
|---------|----------|---|
| 1 | LEDK | LED back light(Cathode) |
| 2 | LEDA | LED back light(Anode) |
| 3 | NC | No Connection |
| 4 - 5 | VSS | Ground |
| 6 | VCI | Supply Voltage (Analog) |
| 7 | IOVCC | Input Voltage (I/O) |
| 8 | BS0 | Select the MCU Interface. Please check table below: |
| 9 | BS1 | |
| 10 | BS2 | |
| 11 | TE | Tearing effect output |
| 12 - 29 | D17 – D0 | Bi-directional data bus for system interface Those unused pins have to open |
| 30 | SDO | Serial data output. If not use, let it to open. |
| 31 | SDI | Serial data input pin. If not used, please let it connected to IOVCC or VSS. |
| 32 | RD | As a read signal and read data at the low level. Fix it to IOVCC or VSS level when using serial interface. |
| 33 | WR | As a write signal and writes data at the rising edge. Fix it to IOVCC or VSS level when using serial interface. |
| 34 | RS/SCL | The signal for command or parameter select under parallel mode. Low : Command High : Parameter When under serial interface, it serves as SCL |
| 35 | CS | Chip select signal. Low: Chip can be accessed. High: Chip cannot be accessed. Must be connected to VSS if not in use |
| 36 | RST | Reset pin. |
| 37 | VSYNC | Frame synchronizing signal. If not used, please let it connected to IOVCC or VSS. |
| 38 | HSYNC | Frame synchronizing signal. If not used, please let it connected to IOVCC or VSS. |
| 39 | PCLK | Dot clock signal. If not used, please let it connected to IOVCC or VSS. |
| 40 | VSS | Ground |
| 41 | DE | Enable data signal If not used, please let it connected to IOVCC or VSS. |



| | | |
|----|---------|--------------------------------|
| 42 | XR / NC | For Resistive Touch Panel Only |
| 43 | YD / NC | |
| 44 | XL / NC | |
| 45 | YU / NC | |

MCU INTERFACE MODE:

| BS2 | BS1 | BS0 | Interface Mode | DB pins |
|-----|-----|-----|---|---------------------------------|
| 0 | 0 | 0 | 16-bit bus interface, 80-system 65K-color | D17-D16: Unused D15-D0: Data |
| 0 | 0 | 1 | 16-bit bus interface, 80-system 262K-color | D17-D16: Unused D15-D0: Data |
| 0 | 1 | 0 | 18-bit bus interface, 80-system 262K-color | D17-D0: Data |
| 0 | 1 | 1 | 8-bit bus interface, 80-system 262K-color | D17-D8: Unused D7-D0: Data |
| 1 | 0 | 0 | 16-bit bus interface, 80-system 262K-color | D17-D16: Unused D15-D0: Data |
| 1 | 0 | 1 | 18-bit bus interface, 80-system 262K-color | D17-D0: Data |
| 1 | 1 | ID | Serial bus IF RGB_EN=0, select by register 72h | SCL,SDO,SDI |

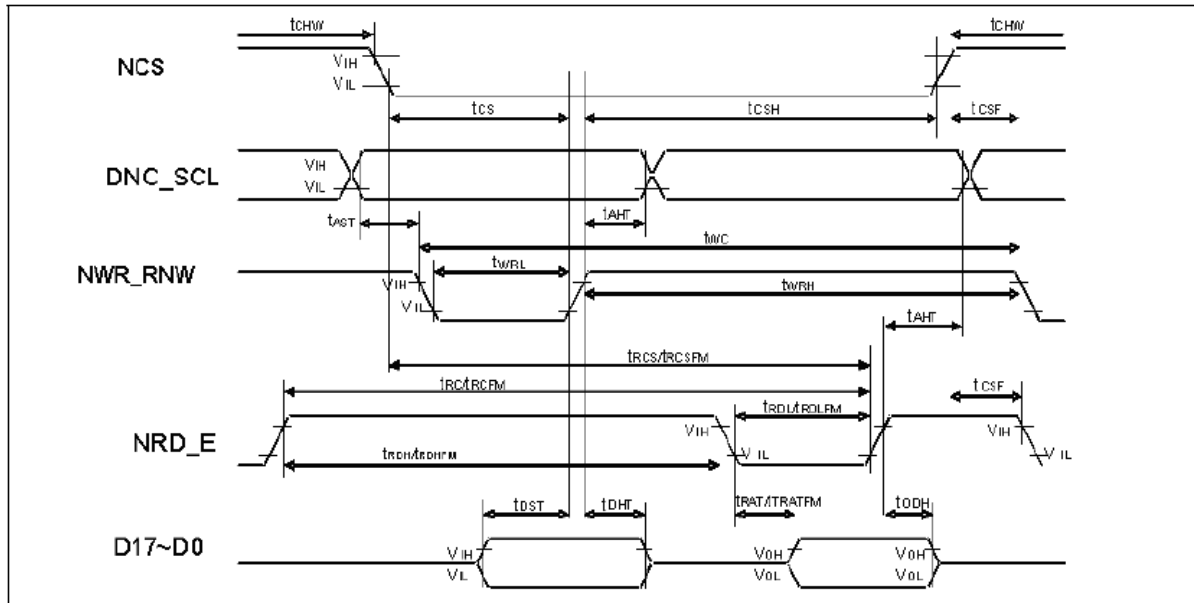
NOTE:

1. About the connection for parallel interface (6800-series), please refer back to our technical support.
2. About the RGB interface mode as active by internal bit RGB_EN setting. For more detail, please refer back to the IC data sheet.



6.0 AC Characteristics

6.1 Parallel Interface Characteristics (8080-series)

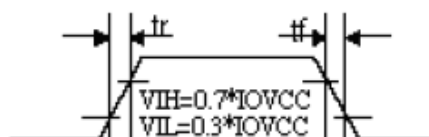


(VSSA=0V, IOVCC=1.65V to 3.3V, VCI=2.3V to 3.3V, Ta = -40 to 85° C)

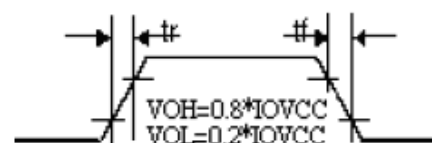
| Signal | Symbol | Parameter | Min. | Max. | Unit | Description |
|-----------|--------|------------------------------------|------|------|------|---|
| DNC_SCL | tAST | Address setup time | 10 | - | ns | - |
| | tAHT | Address hold time (Write/Read) | 10 | - | | |
| NCS | tCHW | Chip select "H" pulse width | 0 | - | ns | - |
| | tCS | Chip select setup time (Write) | 35 | - | | |
| | tRCSFM | Chip select setup time | 180 | - | | |
| | tCSF | Chip select wait time (Write/Read) | 10 | - | | |
| | tCSH | Chip select hold time | 10 | - | | |
| NWR_RNW | tWC | Write cycle | 100 | - | ns | - |
| | tWRH | Control pulse "H" duration | 15 | - | | |
| | tWRL | Control pulse "L" duration | 20 | - | | |
| NRD_E | tRCSFM | Read cycle | 250 | - | ns | When read from GRAM |
| | tRDHFM | Control pulse "H" duration | 15 | - | | |
| | tRDLFM | Control pulse "L" duration | 180 | - | | |
| D17 to D0 | tDST | Data setup time | 10 | - | ns | For maximum CL=30pF For minimum CL=8pF |
| | tDHT | Data hold time | 10 | - | | |
| | tRAT | Read access time (ID) | - | 180 | | |
| | tRATFM | Read access time (FM) | - | 340 | | |
| | tODH | Output disable time | 20 | 80 | | |

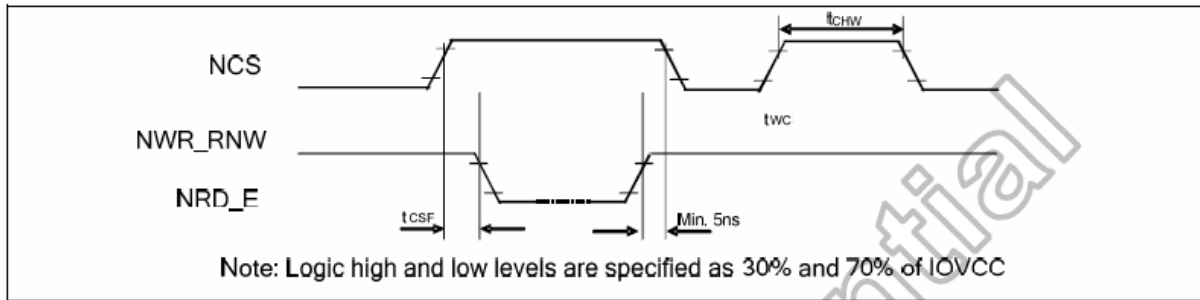
Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less.
Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

Input Signal Slope

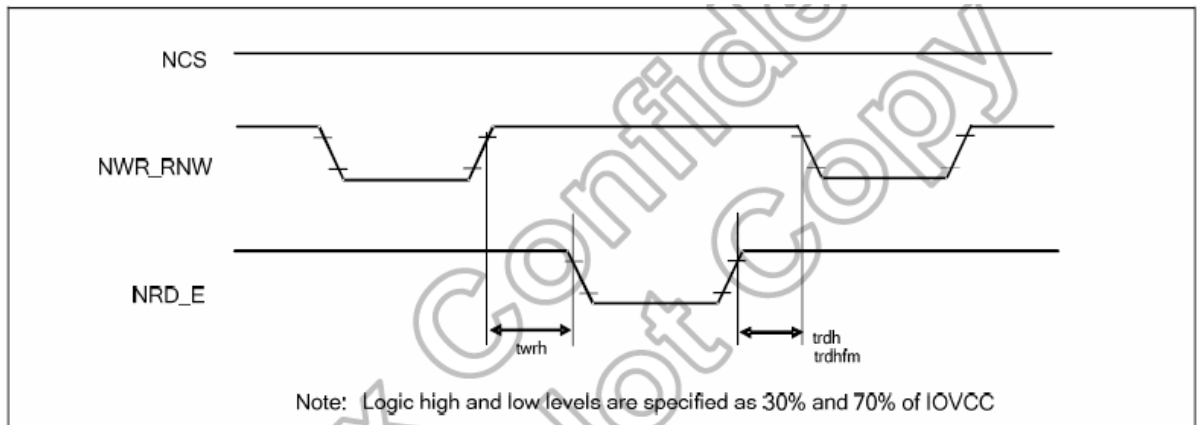


Output Signal Slope





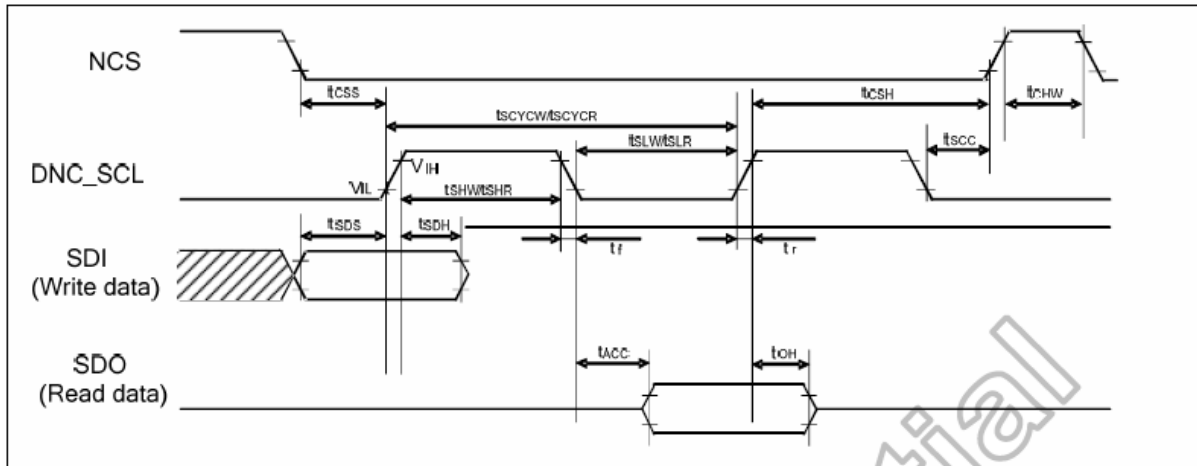
Chip Select Timing



Write to Read and Read to Write Timing



6.2 Serial Interface Characteristics

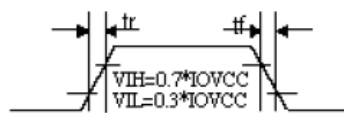


(VSSA=0V, IOVCC=1.65V to 3.3V, VCI=2.3V to 3.3V, Ta = -40 to 85° C)

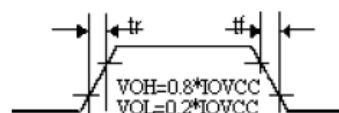
| Parameter | Symbol | Conditions | Min. | Typ. | Maz. | Unit |
|---------------------------------|--------|---|------|------|------|------|
| Serial clock cycle (Write) | tscyww | | 100 | - | - | |
| DNC_SCL "H" pulse width (Write) | tshw | DNC_SCL | 35 | - | - | ns |
| DNC_SCL "L" pulse width (Write) | tslw | DNC_SCL | 35 | - | - | ns |
| Data setup time (Write) | tsds | SDI | 30 | - | - | ns |
| Data hold time (Write) | tsdh | SDI | 30 | - | - | ns |
| Serial clock cycle (Read) | tscywr | | 150 | - | - | |
| DNC_SCL "H" pulse width (Read) | tshr | DNC_SCL | 60 | - | - | ns |
| DNC_SCL "L" pulse width (Read) | tslr | DNC_SCL | 100 | - | - | ns |
| Access Time | tacc | SDO for maximum CL=30pF For minimum CL=8pF | 10 | - | 100 | ns |
| Output disable time | tOH | SDO For maximum CL=30pF For minimum CL=8pF | 15 | - | 100 | ns |
| DNC_SCL to Chip select | tscC | DNC_SCL, NCS | 50 | - | - | ns |
| NCS "H" pulse width | tchW | NCS | 45 | - | - | ns |
| Chip select setup time | tcss | NCS | 60 | - | - | ns |
| Chip select hold time | tCSH | NCS | 80 | - | - | ns |

Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less.
Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

Input Signal Slope

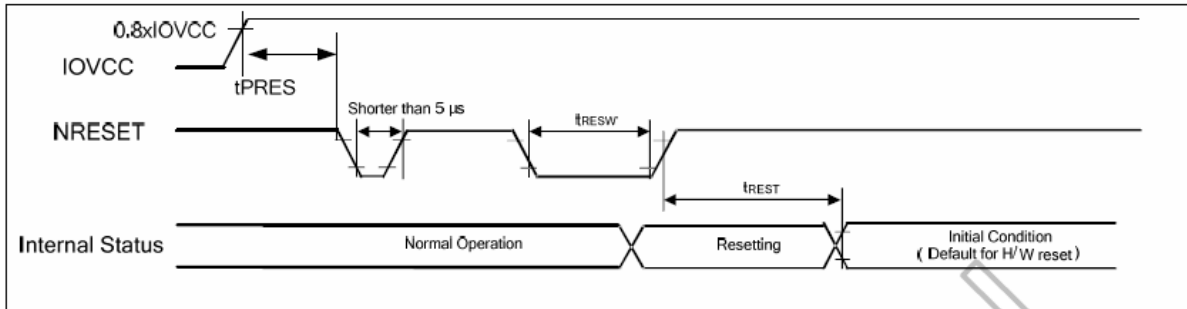


Output Signal Slope





6.3 Reset Input Timing

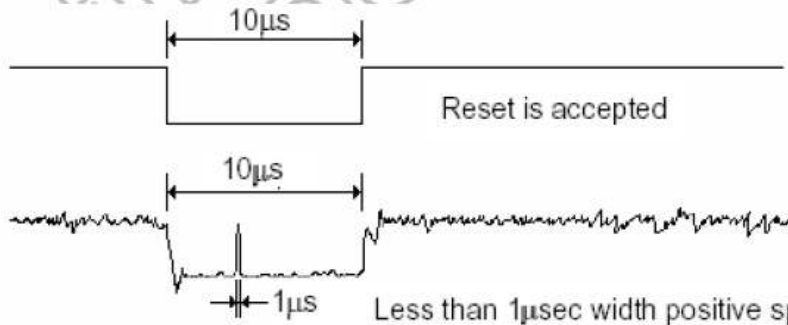


| Symbol | Parameter | Related Pins | Min. | Typ. | Max. | Note | Unit |
|--------|---|----------------|------|------|------|--------------------------------------|------|
| tRESW | Reset low pulse width ⁽¹⁾ | NRESET | 10 | - | - | - | µs |
| tREST | Reset complete time ⁽²⁾ | - | - | - | 5 | When reset applied during STB mode | ms |
| | | - | - | - | 120 | When reset applied during STB mode | ms |
| tPRES | Reset goes high level after Power on time | NRESET & IOVCC | 1 | - | - | Reset goes high level after Power on | ms |

Note: (1) Spike due to an electrostatic discharge on NRESET line does not cause irregular system reset according to the table below.

| NRESET Pulse | Action |
|------------------------|----------------|
| Shorter than 5 µs | Reset Rejected |
| Longer than 10 µs | Reset |
| Between 5 µs and 10 µs | Reset Start |

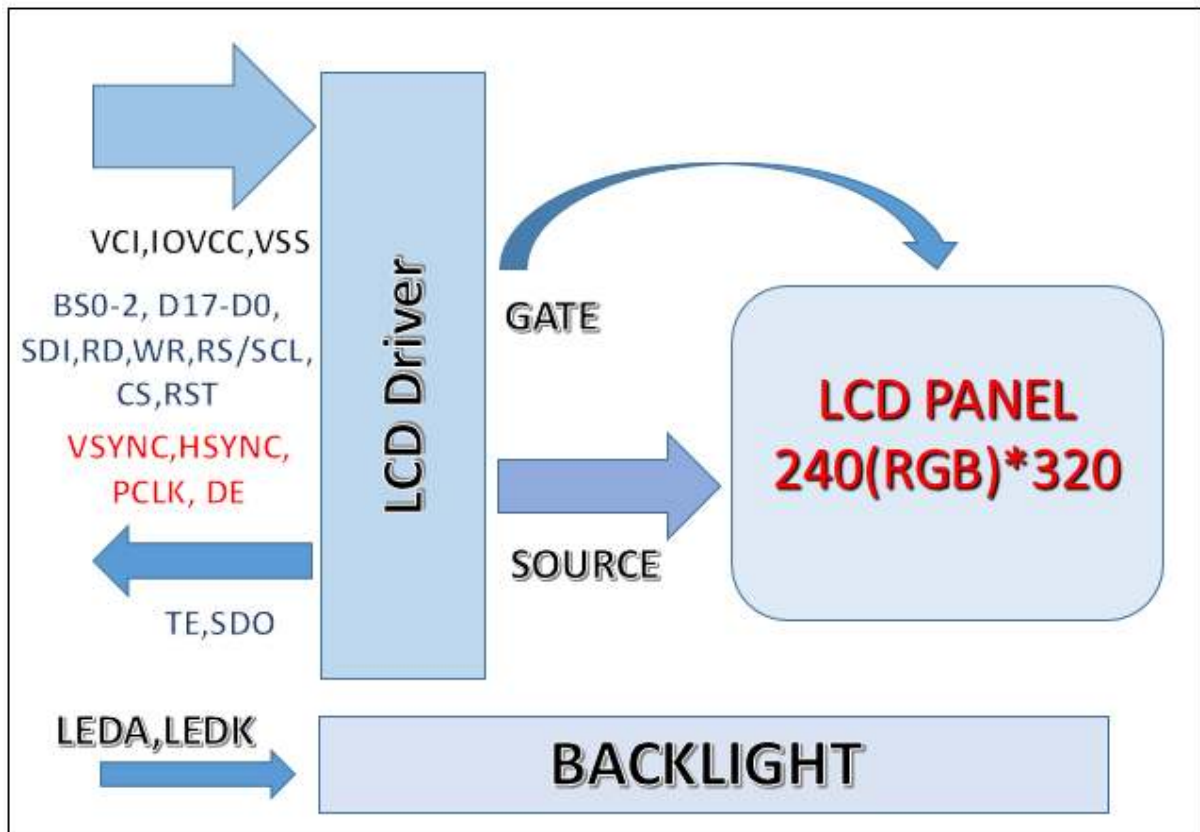
- (2) During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in STB Out –mode. The display remains the blank state in STB –mode) and then return to Default condition for H/W reset.
- (3) During Reset Complete Time, ID2 and VCOMOF value in OTP will be latched to internal register during this period. This loading is done every time when there is H/W reset complete time (tREST) within 5ms after a rising edge of NRESET.
- (4) Spike Rejection also applies during a valid reset pulse as shown below:



- (5) It is necessary to wait 5msec after releasing IRES before sending commands. Also STB Out command cannot be sent for 120msec.



7.0 Block Diagram





8.0 Optical Characteristics

| Items | Symbol | Condition | Min | Typ | Max | Unit | Remark | |
|-------------------------|-------------------|--|--|-------|-------|-------------------|--------|-------|
| Response Time | Tr + Tf | $\Theta = 0^\circ$ $\varnothing = 0^\circ$ Ta = 25°C | - | 30 | 40 | ms | Note5 | |
| Contrast Ratio | Cr | | 500 | 800 | - | - | Note4 | |
| Brightness | | | 300 | 400 | - | cd/m ² | | |
| Uniformity | U | | 70 | - | - | % | | |
| Viewing Angle | $\theta_3 = 90$ | CR>10 | - | 80 | - | ° | Note3 | |
| | $\theta_9 = 270$ | | - | 80 | - | | | |
| | $\theta_{12} = 0$ | | - | 80 | - | | | |
| | $\theta_6 = 180$ | | - | 80 | - | | | |
| CIE (X, Y) Chromaticity | White | X _w | $\Theta = 0^\circ$ $\varnothing = 0^\circ$ Ta = 25°C | 0.284 | 0.304 | 0.32 | - | Note6 |
| | | Y _w | | 0.279 | 0.299 | 0.32 | | |

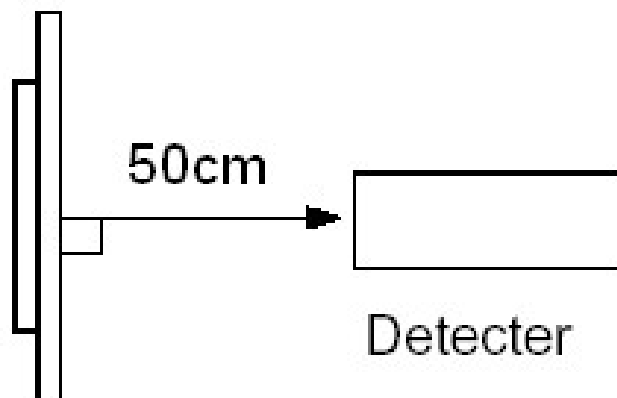
Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25°C.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.





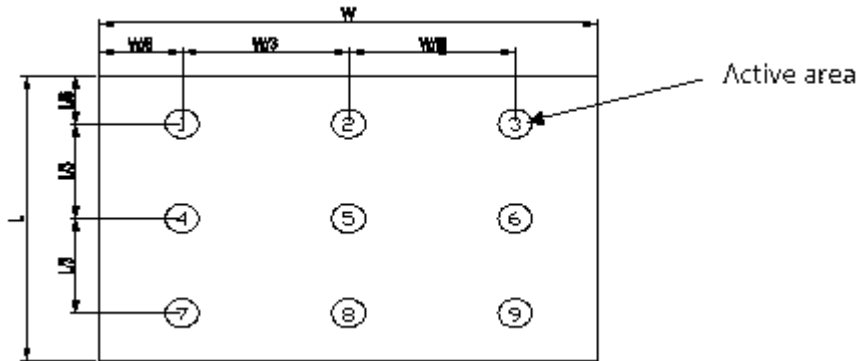
Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$$

Bp (Max.) = Maximum brightness in 9

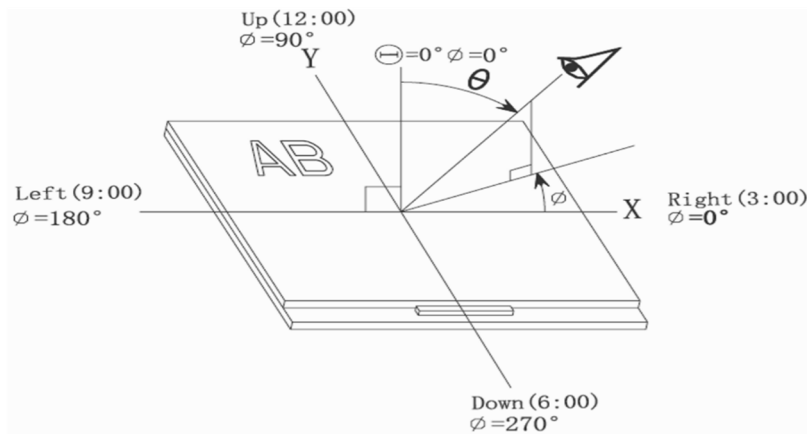
measured spots Bp (Min.) = Minimum

brightness in 9 measured spots.

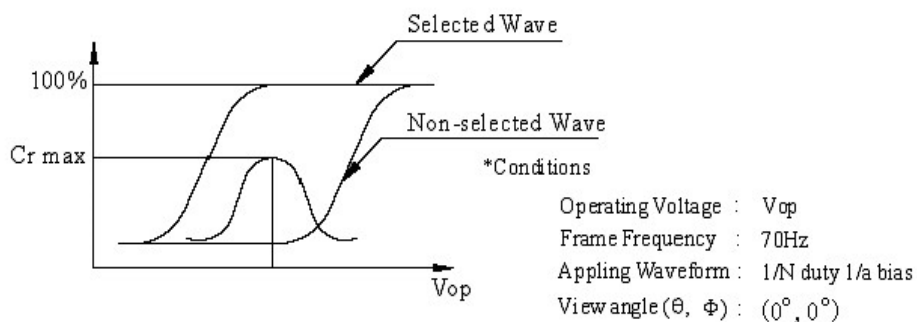


Note 3: The definition of viewing angle:

Refer to the graph below marked by θ and ϕ



Note 4: Definition of contrast ratio. (Test LCD using DMS501)

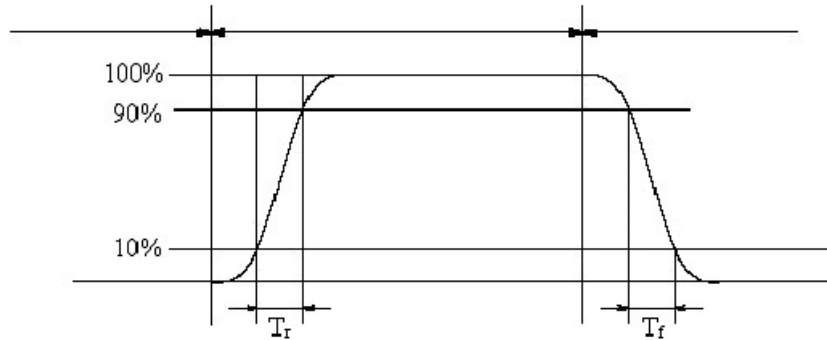


$$\text{Contrast ratio (Cr)} = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$



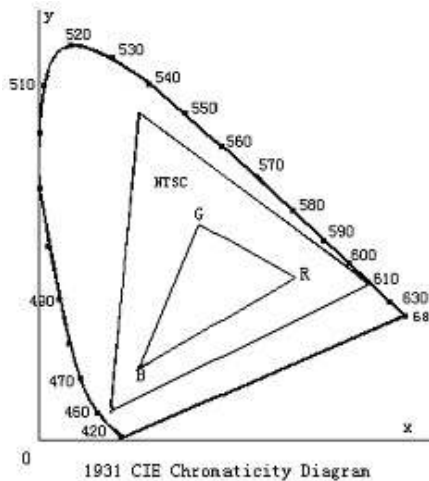
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



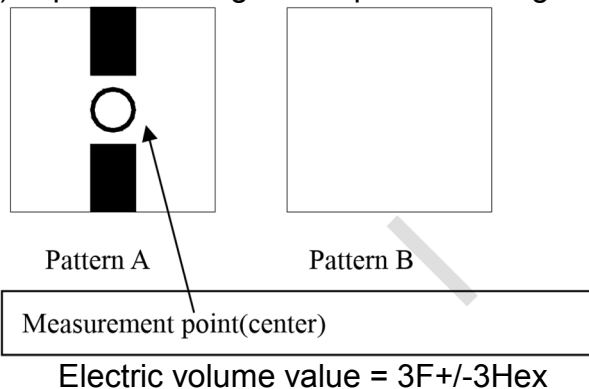
The Definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.



Note 7: Definition of cross talk.

Cross talk ratio (%) = [pattern A Brightness-pattern B Brightness]/pattern A





9.0 Reliability Test Condition

| Item | | Test Condition |
|-----------|------------------------------------|--|
| Operating | High Temperature | 70degC, 240 hrs |
| | Low Temperature | -20degC,240 hrs |
| Storage | High Temperature | 80degC, 240hrs and recovery for 2hrs |
| | Low Temperature | -30degC, 240hrs and recovery for 2hrs |
| | High Temperature and High Humidity | 50degC, 90%RH, 240hrs and recovery for 2 hrs |
| Thermal | Cycle | RT → -30degC → Rt → 80degC → RT 0min 30min 5min 30min 5min 50 cycles (Power off) |
| | Shock | RT → -30degV → 80degC 0min 30min 30min 50 cycles (Power off) |

Note: Rt means Room temperature



10.0 Inspection Criteria

| No | Defect | Definition of defect | Inspection Criteria | | | | | | | | | | | | |
|-------------------------------------|--|---|---|-----------------|-----------------------|-----------------------|---------------------|----|---------------------|-------------------------------------|---|-------------------------|---|--------------|---|
| 1 | a) Definition of dot | <p>The size of defective dot over 1/2 of whole is regards as one defective dot.</p> <p>Smaller than 1/2 Larger than 1/2</p> <p>'No dot defect' (ignore) '1 dot defect' (counted)</p> | <p>A – Viewing Area B – Outside viewing area</p> | | | | | | | | | | | | |
| | b) Bright Dot | Dot appear bright and unchanged in size when LCD panel is displaying black pattern | <table border="1"> <tr> <th>Defect</th> <th>A</th> <th>B</th> </tr> <tr> <td>Bright Dot</td> <td>1</td> <td rowspan="3">NC</td> </tr> <tr> <td>Dark Dot</td> <td>2</td> </tr> <tr> <td>Total</td> <td>3</td> </tr> </table> | Defect | A | B | Bright Dot | 1 | NC | Dark Dot | 2 | Total | 3 | | |
| | Defect | A | B | | | | | | | | | | | | |
| | Bright Dot | 1 | NC | | | | | | | | | | | | |
| Dark Dot | 2 | | | | | | | | | | | | | | |
| Total | 3 | | | | | | | | | | | | | | |
| c) Dark Dot | Dot appear dark and unchanged in size when LCD panel is displaying pure color (RED, GREEN or BLUE) pattern | <table border="1"> <tr> <th>Defect</th> <th>A</th> <th>B</th> </tr> <tr> <td>2 Bright dot Adjacent</td> <td></td> <td rowspan="2">NC</td> </tr> <tr> <td>2 Dark dot Adjacent</td> <td>1</td> </tr> </table> <p>NC – Not Count</p> | Defect | A | B | 2 Bright dot Adjacent | | NC | 2 Dark dot Adjacent | 1 | | | | | |
| Defect | A | B | | | | | | | | | | | | | |
| 2 Bright dot Adjacent | | NC | | | | | | | | | | | | | |
| 2 Dark dot Adjacent | 1 | | | | | | | | | | | | | | |
| d) 2 dot adjacent | <p>1 pair = 2 dots</p> <p>Type 1 Type 2</p> <p>or</p> <p>Type 3</p> | <table border="1"> <tr> <th>Defect</th> <th>Acc. Count</th> </tr> <tr> <td>2 Bright dot Adjacent</td> <td>0</td> </tr> <tr> <td>2 Dark dot Adjacent</td> <td>1</td> </tr> </table> | Defect | Acc. Count | 2 Bright dot Adjacent | 0 | 2 Dark dot Adjacent | 1 | | | | | | | |
| Defect | Acc. Count | | | | | | | | | | | | | | |
| 2 Bright dot Adjacent | 0 | | | | | | | | | | | | | | |
| 2 Dark dot Adjacent | 1 | | | | | | | | | | | | | | |
| 2 | <p>Black spot White Spot Bright spot Pin Hole Foreign Particle</p> | <p>-Black/Dark/Bright Spot is points on display which appear dark/bright and usually result from contamination - These defect do not vary in size intensity (contrast) when kontras is varied.</p> | <table border="1"> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> <tr> <td>$D \leq 0.10$</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>$0.10 \leq D \leq 0.15$</td> <td>2</td> </tr> <tr> <td>$0.15 \leq D \leq 0.20$</td> <td>1</td> </tr> <tr> <td>$D \geq 0.2$</td> <td>0</td> </tr> </table> | Defect Category | A | B | $D \leq 0.10$ | NC | NC | $0.10 \leq D \leq 0.15$ | 2 | $0.15 \leq D \leq 0.20$ | 1 | $D \geq 0.2$ | 0 |
| Defect Category | A | B | | | | | | | | | | | | | |
| $D \leq 0.10$ | NC | NC | | | | | | | | | | | | | |
| $0.10 \leq D \leq 0.15$ | 2 | | | | | | | | | | | | | | |
| $0.15 \leq D \leq 0.20$ | 1 | | | | | | | | | | | | | | |
| $D \geq 0.2$ | 0 | | | | | | | | | | | | | | |
| 3 | <p>Black Line White line Particle between POL and Glass Scratch on Glass</p> | | <table border="1"> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> <tr> <td>$W \leq 0.03$</td> <td>NC</td> <td rowspan="3">NC</td> </tr> <tr> <td>$0.03 \leq W \leq 0.05, L \leq 2.0$</td> <td>2</td> </tr> <tr> <td>$W \geq 0.05$</td> <td>0</td> </tr> </table> | Defect Category | A | B | $W \leq 0.03$ | NC | NC | $0.03 \leq W \leq 0.05, L \leq 2.0$ | 2 | $W \geq 0.05$ | 0 | | |
| Defect Category | A | B | | | | | | | | | | | | | |
| $W \leq 0.03$ | NC | NC | | | | | | | | | | | | | |
| $0.03 \leq W \leq 0.05, L \leq 2.0$ | 2 | | | | | | | | | | | | | | |
| $W \geq 0.05$ | 0 | | | | | | | | | | | | | | |
| 4 | <p>POL Bubble POL Dented</p> | | <table border="1"> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> <tr> <td>$D \leq 0.20$</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>$0.20 \leq D \leq 0.30$</td> <td>3</td> </tr> <tr> <td>$0.30 \leq D \leq 0.50$</td> <td>2</td> </tr> <tr> <td>$D \geq 0.5$</td> <td>0</td> </tr> </table> | Defect Category | A | B | $D \leq 0.20$ | NC | NC | $0.20 \leq D \leq 0.30$ | 3 | $0.30 \leq D \leq 0.50$ | 2 | $D \geq 0.5$ | 0 |
| Defect Category | A | B | | | | | | | | | | | | | |
| $D \leq 0.20$ | NC | NC | | | | | | | | | | | | | |
| $0.20 \leq D \leq 0.30$ | 3 | | | | | | | | | | | | | | |
| $0.30 \leq D \leq 0.50$ | 2 | | | | | | | | | | | | | | |
| $D \geq 0.5$ | 0 | | | | | | | | | | | | | | |
| 5 | <p>Mura (50% Grey)</p> | | Judged by Limit sample | | | | | | | | | | | | |



11.0 Precaution and Limited Warranty

1. Handling Precautions

- a. The display panel is made of glass and polarizer. As glass is fragile. It tends to chip during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock of impact or by dropping it.
- b. If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance is in contact with your skin or clothes, wash it off using soap and water.
- c. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degrade the insulation between terminals. Scratch and dents may occur on polarizer too.
- d. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- e. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol
 - Do not scrub hard to avoid damaging the display surface.
- f. Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents
 - Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.
- g. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- h. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- i. Do not attempt to disassemble or process the LCD module.
- j. NC terminal should be open. Do not connect anything.
- k. If the logic circuit power is off, do not apply the input signals.
- l. Electro-Static Discharge Control. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembly, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screw driver should be of ground potentiality to minimize as much as possible any



transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work environment is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- m. Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist the LCM.

2. Storage Precautions

When storing the LCD modules, the following precaution are necessary.

- a. Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- b. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- c. The polarizer surface should not come in contact with any other objects.

3. Others

- a. Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- b. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- c. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc. Exercise care to avoid holding the following sections when handling the modules.
 - Exposed area of the printed circuit board.
 - Terminal electrode sections.

4. Using LCD Modules

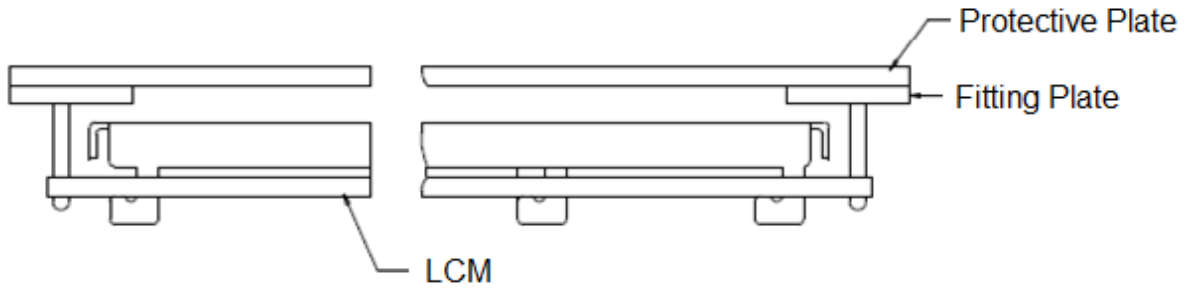
a. Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below.

Attend to the following items when installing the LCM.



- b. Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- c. When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.
- d. Precaution for assemble the module with BTB connector:
Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



5. Precaution for soldering the LCM

| | Manual soldering | Machine drag soldering | Machine press soldering |
|-----------------|------------------------------|-----------------------------------|---|
| No RoHS Product | 290°C ~350°C. Time: 3-5S. | 330°C ~350°C. Speed: 4-8 mm/s. | 300°C ~330°C. Time: 3-6S. Press: 0.8~1.2Mpa |
| RoHS Product | 340°C ~370°C. Time: 3-5S. | 350°C ~370°C. Time: 4-8 mm/s. | 330°C ~360°C. Time: 3-6S. Press: 0.8~1.2Mpa |

- a. If soldering flux is used, be sure to remove any remaining flux after finishing the soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- b. When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- c. When removing the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

6. Precautions for Operation

- a. Viewing angle varies with the change of liquid crystal driving voltage (VLCD).
Adjust VLCD to show the best contrast.



- b. It is recommended to drive LCD's within the specified voltage limit since over limit will cause shorter LCD life. An electrochemical reaction due to direct current causes LCD-deterioration. Avoid the use of direct current drive.
- c. Response time will be extremely delayed at lower temperature compared to room operating temperature range and on the other hand, at higher temperature LCD shows dark color in them. However those phenomena do not mean malfunction. The LCD will return to normal performance when ambient temperature revert to room condition.
- d. If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and on.
- e. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
- f. Input logic voltage before apply analogue high voltage such as LCD driving voltage when power on. Remove analogue high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- g. Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

7. Safety

- a. It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

8. Limited Warranty

Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.

9. Return LCM under Warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass
- PCB eyelet's damaged or modified
- PCB conductors damaged
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to, or modifying the bezel in any manner.

Module repairs will be invoiced to customer upon mutual agreement. Modules must be returned with sufficient description of failure or defects. Any connectors or cable installed by customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.

