

# Crystal Clear Technology

## Product Specification

**T500A12X00**

(REVISION 2)

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2. Record of Revision

Rev	Date	Item	Page	Comment	Originator	Checked By
1	22/11/17			Initial Release	Azhar	WHong
2	07/05/18			Update mechanical drawing	Azhar	WHong



3. General Specification

T500A12X00 is 5.0" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs control circuit, LED backlight. This display area contains 800(RGB) x 480 pixels and can display up to 16M colors. This product compliant with RoHS environmental requirement.

Item	Specifications	Unit	Remarks
Size	5.0" Diagonal		
Display type	16.7M color TFT, Normally Black		1
Viewing direction	All Direction	O 'Clock	
Outline dimensions	121.0(W) x 77.4(H) x 4.10(T)		2
Active Area	108(W) x 64.8(H)	mm	
Resolution	800(RGB) x 480	mm	
Pixel Size	0.135(W) x 0.135 (H)	mm	
Pixel arrangement	RGB vertical strip		
Driving IC	HX8249-A01-LT & HX8678-C-LT or Equivalent		
Interface Mode	RGB		
Backlight	21 Chips white LED	Pcs	
Luminance	1000	cd/m2	
Operating temperature	-20°C ~ 70°C		
Storage temperature	-30°C ~ 80°C		

Note1: Color tone is slightly changed by temperature and driving voltage.

Note2: FPC or Wire or Foam Tape are not included.



4. Absolute Maximum Ratings

1.1 Electrical Absolute Maximum ratings (Vss = 0V, Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Remarks
Power for Circuit Driving	VDD	-0.3	-	4.0	V	
LED Forward Current	If	-	-	35	mA	
Storage Humidity	HST	10	-	90	%RH	At 25±5°C
Storage Temperature	TST	-30	-	80	°C	
Operating Ambient Humidity	H OP	10	-	90	%RH	
Operating Ambient temperature	TOP	-20	-	70	°C	

Note:

1. Temp.>60°C, Absolute humidity shall be less than 90%RH at 60°C
2. Temp.≤60°C, 90%RH MAX



5. Electrical Characteristics

5.1 Electrical Characteristics (Vss = 0V, Ta = 25°C)

Properties		Sym	Min	Typ	Max	Unit	Note
Power for Circuit Driving		VDD	2.7	-	3.6	V	Note
Logic Input Voltage	Low Voltage	VIL	-0.3	-	0.3VDD	V	
	High Voltage	VIH	0.7VDD	-	VDD+0.3	V	
Logic Output Voltage	Low Voltage	VOL	0	-	0.4	V	
	High Voltage	VOH	VDD-0.4	-	-	V	

Note:

The recommended operating conditions refer to a range in which operation of this product is guaranteed. Accordingly, please make sure that the module is used within this range. And these current values are measured under the condition that all devices are stopped, each component is stable and logic signal is input.

5.2 LED Backlight Specification (Vss = 0V, Ta = 25°C)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	Vf	Ta=25 °C, If=140mA	16.4	18.0	19.8	V
Power Supply Current	If	Ta=25°C	-	140	-	mA
Luminous intensity	Lv	White LED	900	1000	-	cd/m <sup>2</sup>
Half life expectancy	LL	If=140mA	30000			Hrs
Drive method	Constant current					
LED configuration	21 White LED ,3 in series ,7 in Parallel					



5.3 Pin Assignment Table

Pin No.	Symbol	Description
1	VLED-	Backlight -ve
2	VLED+	Backlight +ve
3	GND	Ground
4	VDD	Power Supply
5-12	R0~R7	Red Data Bus (R0 ~ R7)
13-20	G0~G7	Green Data Bus (G0 ~ G7)
21-28	B0~B7	Blue Data Bus (B0 ~ B7)
29	GND	Ground
30	PCLK	Data Clock
31	DISP	Display ON/OFF
32	HSYNC	Line Synchronization Signal
33	VSYNC	Frame Synchronization Signal
34	DE	Data Enable
35	NC	No Connection
36	GND	Ground
37	RST	Reset
38	VDD	Power Supply
39	GND	Ground
40	INT	Interrupt
41	SDA	I2C Serial Data
42	SCL	I2C Serial Clock
43	NC	No Connection
44	GND	Ground
45	GND	Ground

CTP

6. Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp	$\theta=0^\circ$	900	1000	-	Cd/m <sup>2</sup>	1
Uniformity	$\Delta Bp$	$\Phi=0^\circ$	70	80	-	%	1,2
Viewing Angle	3:00	Cr $\geq$ 10	-	80	-	Deg	3
	6:00		-	80	-		
	9:00		-	80	-		
	12:00		-	80	-		
Contrast Ratio	Cr	$\theta=0^\circ$ $\Phi=0^\circ$	600	800	-	-	4
Response Time	T <sub>r</sub>		-	20	-	ms	5
	T <sub>f</sub>		-	20	-	ms	
NTSC Ratio	S		45	60	-	%	

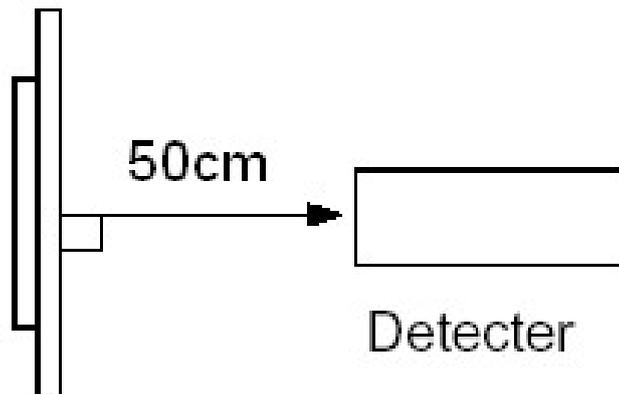
Note: The parameter is slightly changed by temperature, driving voltage and material

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25°C.
- Adjust operating voltage to get optimum contrast at the center of the display.

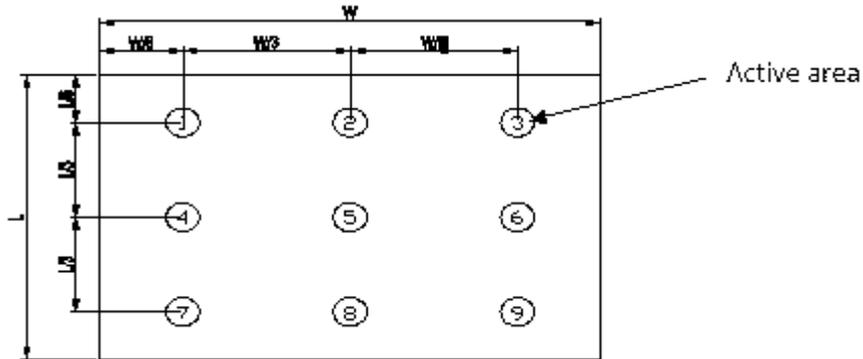
Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.



Note 2: The luminance uniformity is calculated by using following formula.

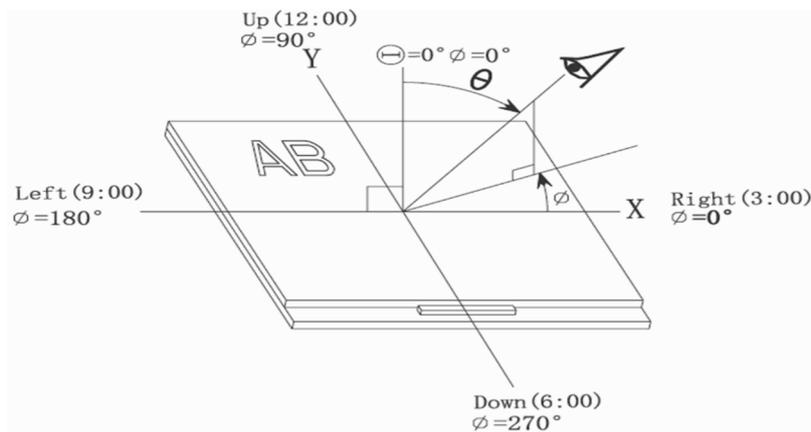
$$\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$$

$Bp (\text{Max.})$  = Maximum brightness in 9 measured spots  
 $Bp (\text{Min.})$  = Minimum brightness in 9 measured spots.

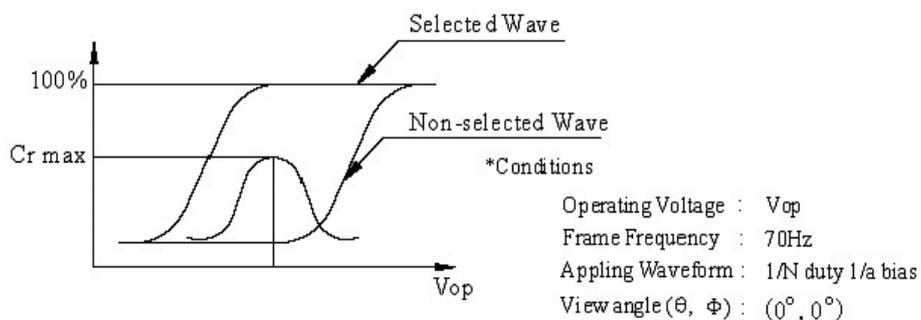


Note 3: The definition of viewing angle:

Refer to the graph below marked by  $\theta$  and  $\phi$



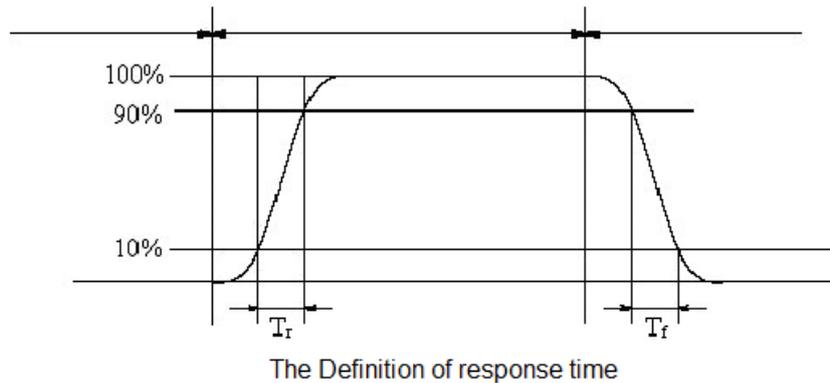
Note 4: Definition of contrast ratio. (Test LCD using DMS501)



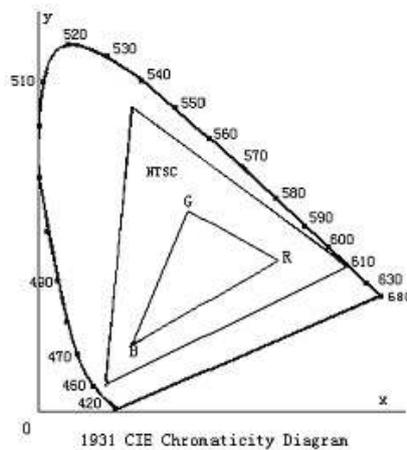
$$\text{Contrast ratio (Cr)} = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.

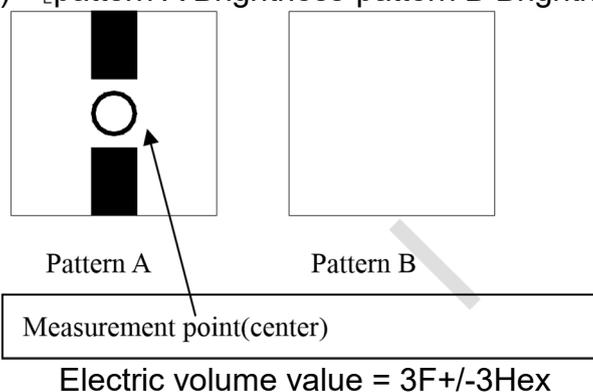


Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.



Note 7: Definition of cross talk.

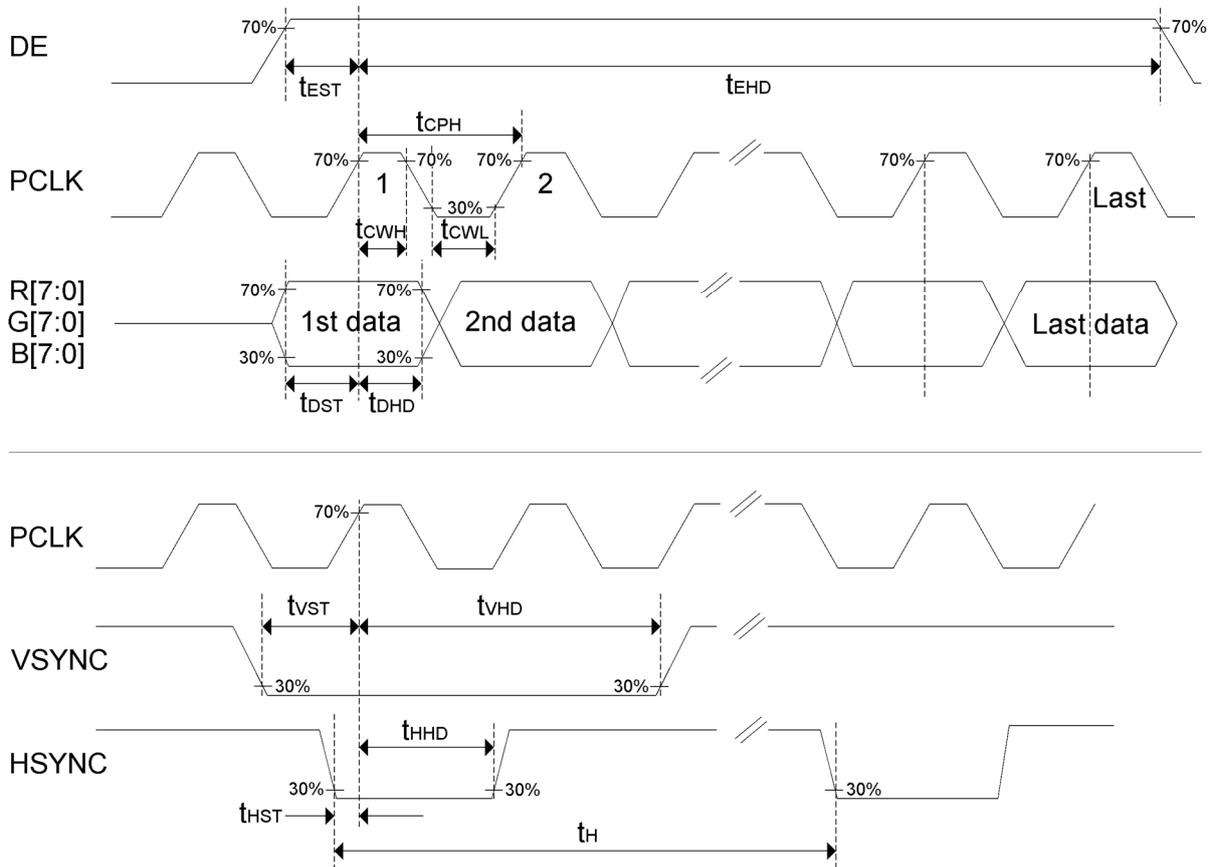
$$\text{Cross talk ratio (\%)} = \frac{[\text{pattern A Brightness} - \text{pattern B Brightness}]}{\text{pattern A}}$$





7. Timing Characteristics

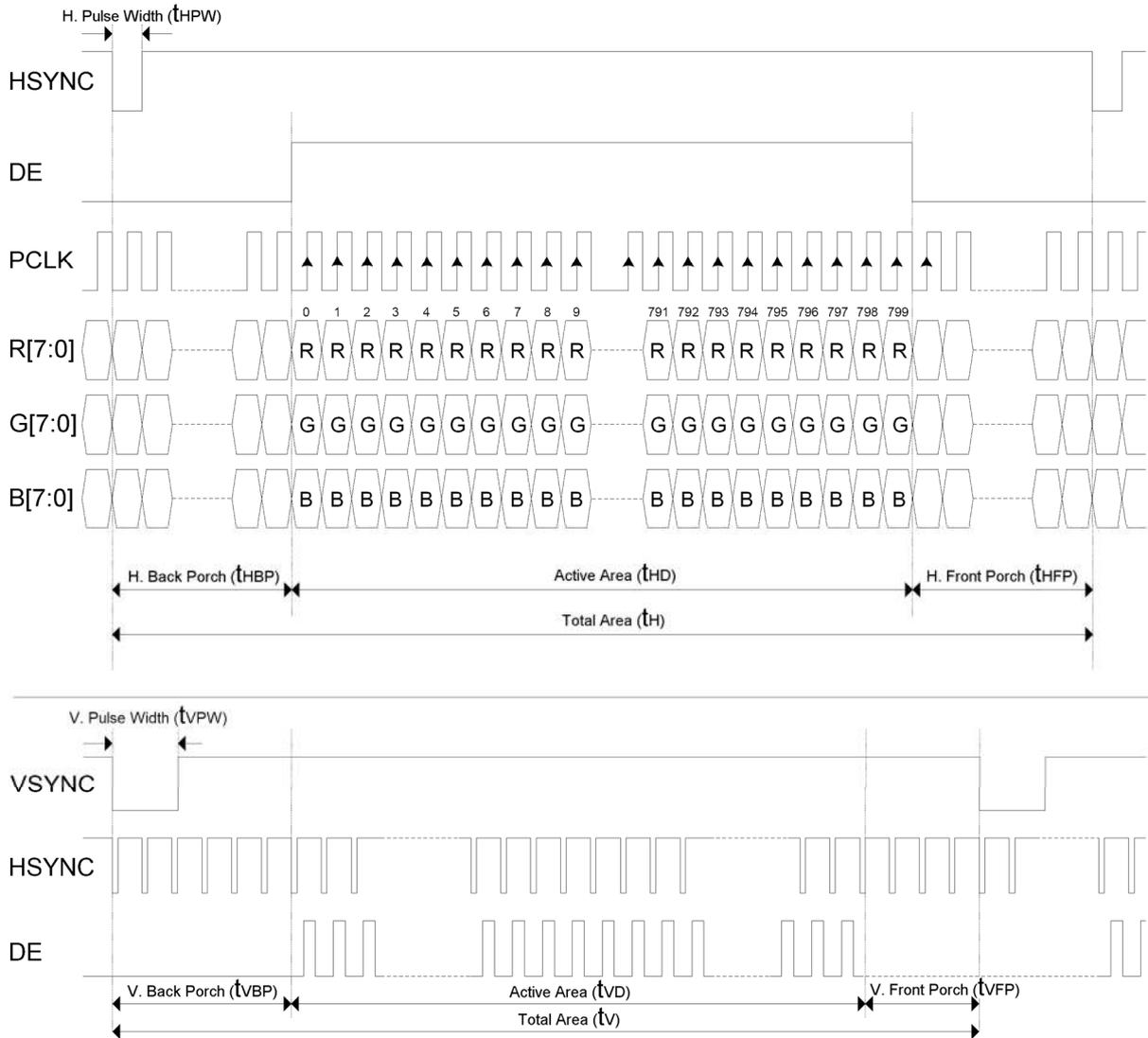
7.1 AC Timing Characteristics



Parameter	Symbol	Specification			Unit	Conditions
		Min.	Typ.	Max.		
PCLK cycle time	tCPH	16.8	-	-	ns	
PCLK pulse duty	tCWH/tCWL	40	50	60	%	
VSYNC setup time	tVST	5	-	-	ns	
VSYNC hold time	tVHD	5	-	-	ns	
HSYNC setup time	tHST	5	-	-	ns	
HSYNC hold time	tHHD	5	-	-	ns	
DATA setup time	tDST	5	-	-	ns	R[7:0],G[7:0],B[7:0] to PCLK
DATA hold time	tDHD	5	-	-	ns	R[7:0],G[7:0],B[7:0] to PCLK
DE setup time	tEST	5	-	-	ns	
DE hold time	tEHD	5	-	-	ns	



7.2 Display Timing Characteristics



Parameter	Symbol	Specification			Unit	Conditions
		Min.	Typ.	Max.		
PCLK frequency	f <sub>CLK</sub>	25.2	27.2	30.5	MHz	
HSYNC active area	t <sub>HD</sub>	-	800	-	PCLK	
HSYNC period time	t <sub>H</sub>	856	860	920	PCLK	
HSYNC pulse width	t <sub>HPW</sub>	1	2	100	PCLK	
HSYNC back porch	t <sub>HBP</sub>	5	16	101	PCLK	
HSYNC front porch	t <sub>HFP</sub>	19	44	115	PCLK	
VSYNC active area	t <sub>VD</sub>	-	480	-	HSYNC	
VSYNC period time	t <sub>V</sub>	490	528	552	HSYNC	
VSYNC pulse width	t <sub>VPW</sub>	1	2	66	HSYNC	
VSYNC back porch	t <sub>VBP</sub>	5	5	67	HSYNC	
VSYNC front porch	t <sub>VFP</sub>	5	43	67	HSYNC	



7.3 Reset Operation

Item	Symbol	Unit	Min	Typ	Max
Reset low level width	tRES	us	10	-	-
Reset rise time	trRES	us	-	-	5



8. Reliability Test Condition

Item		Test Condition
Operating	High Temperature	70degC, 240 hrs
	Low Temperature	-20degC,240 hrs
Storage	High Temperature	80degC, 240hrs and recovery for 2hrs
	Low Temperature	-30degC, 240hrs and recovery for 2hrs
	High Temperature and High Humidity	50degC, 90%RH, 240hrs and recovery for 2 hrs
Thermal	Cycle	RT → -20degC → Rt → 70degC → RT 0min 30min 5min 30min 5min 50 cycles (Power off)
	Shock	RT → -20degV → 70degC 0min 30min 30min 50 cycles (Power off)

Note: RT means Room temperature

9. Inspection Criteria

No	Defect	Definition of defect	Inspection Criteria														
1	a) Definition of dot	<p>The size of defective dot over 1/2 of whole is regards as one defective dot.</p> <p>Smaller than 1/2      Larger than 1/2</p> <p>R G B      R G B</p> <p>'No dot defect' (ignore)      '1 dot defect' (counted)</p>	<p>A – Viewing area B – Viewing area C – Outside Viewing area</p>														
	b) Bright Dot	Dot appear bright and unchanged in size when LCD panel is displaying black pattern	<table border="1"> <thead> <tr> <th>Defect</th> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>Bright Dot</td> <td>1</td> <td>1</td> <td rowspan="3">NC</td> </tr> <tr> <td>Dark Dot</td> <td>2</td> <td>2</td> </tr> <tr> <td>Total</td> <td colspan="2">4</td> </tr> </tbody> </table> <p>NC – Not Count</p>	Defect	A	B	C	Bright Dot	1	1	NC	Dark Dot	2	2	Total	4	
	Defect	A		B	C												
	Bright Dot	1	1	NC													
Dark Dot	2	2															
Total	4																
c) Dark Dot	Dot appear dark and unchanged in size when LCD panel is displaying pure color (RED, GREEN or BLUE) pattern																
d) 2 dot adjacent	<p>1 pair = 2 dots</p> <p>Type 1      Type 2</p> <p>or</p> <p>Type 3</p>	<table border="1"> <thead> <tr> <th>Defect</th> <th>Acc. Count</th> </tr> </thead> <tbody> <tr> <td>2 Bright dot Adjacent</td> <td>0</td> </tr> <tr> <td>2 Dark dot Adjacent</td> <td>1</td> </tr> </tbody> </table>	Defect	Acc. Count	2 Bright dot Adjacent	0	2 Dark dot Adjacent	1									
Defect	Acc. Count																
2 Bright dot Adjacent	0																
2 Dark dot Adjacent	1																
2	<p>Black spot White Spot Bright spot Pin Hole Foreign Particle</p>	<p>-Black/Dark/Bright Spot is points on display which appear dark/bright and usually result from contamination - These defect do not vary in size intensity (contrast) when kontras is varied.</p> <p><math>D=(a+b)/2(mm)</math></p>	<p>A: Viewing area B: Outside Viewing area</p> <table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>D &lt; 0.10</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>0.10 ≤ D &lt; 0.20</td> <td>2</td> </tr> <tr> <td>0.20 ≤ D ≤ 0.30</td> <td>1</td> </tr> <tr> <td>D &gt; 0.30</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	D < 0.10	NC	NC	0.10 ≤ D < 0.20	2	0.20 ≤ D ≤ 0.30	1	D > 0.30	0		
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0.20 ≤ D ≤ 0.30	1																
D > 0.30	0																
<p>Black Line White line Particle between POL and Glass Scratch on Glass</p>		<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>W &lt; 0.03</td> <td>NC</td> <td rowspan="3">NC</td> </tr> <tr> <td>0.03 ≤ W ≤ 0.08, L ≤ 2.0</td> <td>2</td> </tr> <tr> <td>W &gt; 0.08</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	W < 0.03	NC	NC	0.03 ≤ W ≤ 0.08, L ≤ 2.0	2	W > 0.08	0					
Defect Category	A	B															
W < 0.03	NC	NC															
0.03 ≤ W ≤ 0.08, L ≤ 2.0	2																
W > 0.08	0																
<p>POL Bubble POL Dented</p>		<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>D &lt; 0.20</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>0.20 ≤ D &lt; 0.30</td> <td>3</td> </tr> <tr> <td>0.30 ≤ D ≤ 0.50</td> <td>2</td> </tr> <tr> <td>D &gt; 0.5</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	D < 0.20	NC	NC	0.20 ≤ D < 0.30	3	0.30 ≤ D ≤ 0.50	2	D > 0.5	0			
Defect Category	A	B															
D < 0.20	NC	NC															
0.20 ≤ D < 0.30	3																
0.30 ≤ D ≤ 0.50	2																
D > 0.5	0																
3	Mura (50% Grey)		Judged by Limit sample														



10. Precaution for Using TFT Modules

1. Handling Precautions

- a. The display panel is made of glass and polarizer. As glass is fragile. It tends to chip during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock of impact or by dropping it.
- b. If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance is in contact with your skin or clothes, wash it off using soap and water.
- c. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degrade the insulation between terminals. Scratch and dents may occur on polarizer too.
- d. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- e. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol
  - Do not scrub hard to avoid damaging the display surface.
- f. Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents
  - Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or colour fading. Avoid contact with oil and fats.
- g. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- h. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- i. Do not attempt to disassemble or process the LCD module.
- j. NC terminal should be open. Do not connect anything.
- k. If the logic circuit power is off, do not apply the input signals.
- l. Electro-Static Discharge Control. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
  - Tools required for assembly, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screw driver



should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work environment is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- m. Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
  - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
  - Do not damage or modify the pattern writing on the printed circuit board.
  - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
  - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
  - Do not drop, bend or twist the LCM.

## 2. Storage Precautions

When storing the LCD modules, the following precaution are necessary.

- a. Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- b. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- c. The polarizer surface should not come in contact with any other objects.

## 3. Others

- a. Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- b. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- c. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc. Exercise care to avoid holding the following sections when handling the modules.
  - Exposed area of the printed circuit board.
  - Terminal electrode sections.

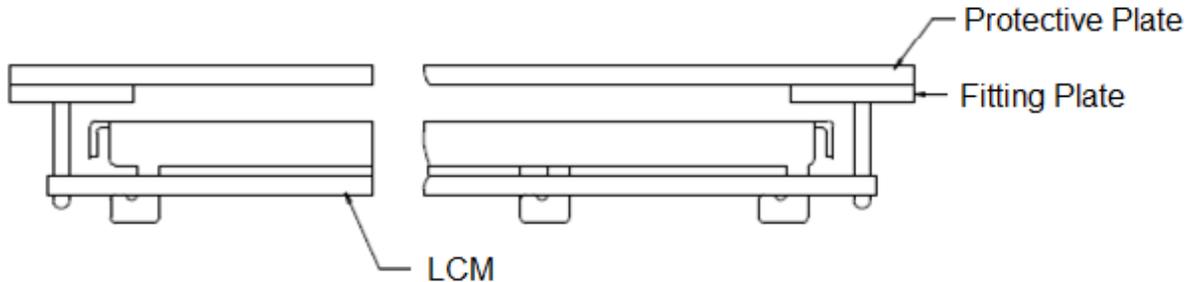
## 4. Using LCD Modules

### a. Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below.

Attend to the following items when installing the LCM.

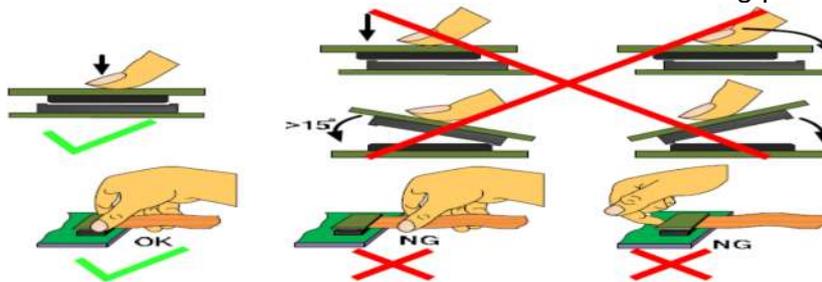
- b. Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- c. When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.

- d. Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



5. Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C. Time: 3-5S.	330°C ~350°C. Speed: 4-8 mm/s.	300°C ~330°C. Time: 3-6S. Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C. Time: 3-5S.	350°C ~370°C. Time: 4-8 mm/s.	330°C ~360°C. Time: 3-6S. Press: 0.8~1.2Mpa

- a. If soldering flux is used, be sure to remove any remaining flux after finishing the soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- b. When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- c. When removing the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

6. Precautions for Operation

- a. Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.



- b. It is recommended to drive LCD's within the specified voltage limit since over limit will cause shorter LCD life. An electrochemical reaction due to direct current causes LCD-deterioration. Avoid the use of direct current drive.
- c. Response time will be extremely delayed at lower temperature compared to room operating temperature range and on the other hand, at higher temperature LCD shows dark colour in them. However those phenomena do not mean malfunction. The LCD will return to normal performance when ambient temperature revert to room condition.
- d. If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and on.
- e. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
- f. Input logic voltage before apply analogue high voltage such as LCD driving voltage when power on. Remove analogue high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- g. Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

7. Safety

- a. It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

8. Limited Warranty

Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.

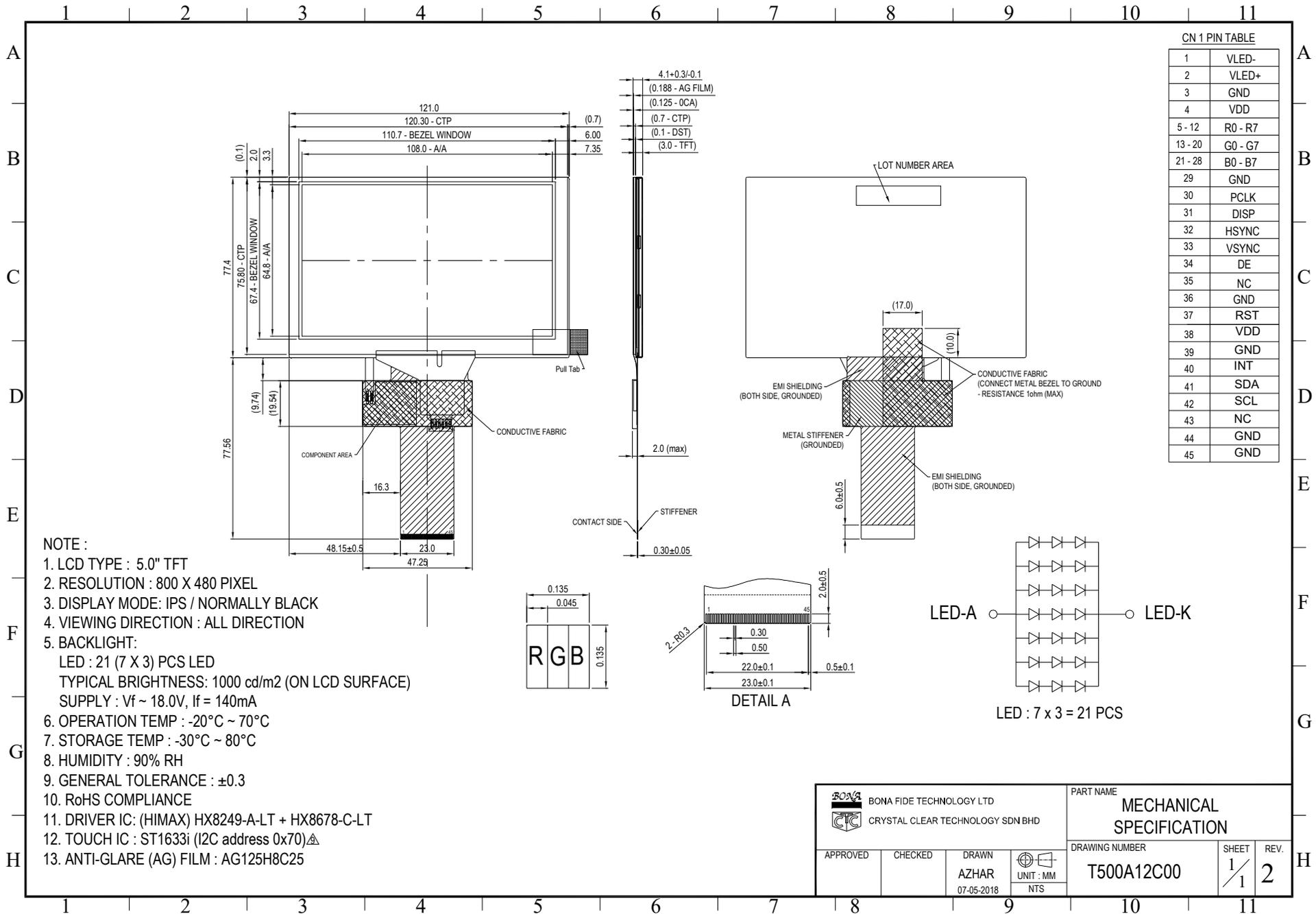
9. Return LCM under Warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass
- PCB eyelet's damaged or modified
- PCB conductors damaged
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to, or modifying the bezel in any manner.

Module repairs will be invoiced to customer upon mutual agreement. Modules must be returned with sufficient description of failure or defects. Any connectors or cable installed by customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.

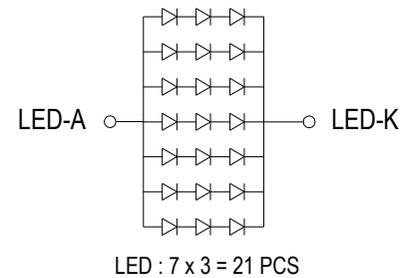
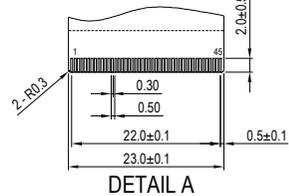
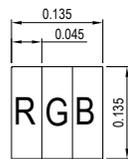




CN 1 PIN TABLE	
1	VLED-
2	VLED+
3	GND
4	VDD
5-12	R0 - R7
13-20	G0 - G7
21-28	B0 - B7
29	GND
30	PCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	RST
38	VDD
39	GND
40	INT
41	SDA
42	SCL
43	NC
44	GND
45	GND

**NOTE :**

- LCD TYPE : 5.0" TFT
- RESOLUTION : 800 X 480 PIXEL
- DISPLAY MODE: IPS / NORMALLY BLACK
- VIEWING DIRECTION : ALL DIRECTION
- BACKLIGHT:
  - LED : 21 (7 X 3) PCS LED
  - TYPICAL BRIGHTNESS: 1000 cd/m2 (ON LCD SURFACE)
  - SUPPLY : Vf ~ 18.0V, If = 140mA
- OPERATION TEMP : -20°C ~ 70°C
- STORAGE TEMP : -30°C ~ 80°C
- HUMIDITY : 90% RH
- GENERAL TOLERANCE : ±0.3
- RoHS COMPLIANCE
- DRIVER IC: (HIMAX) HX8249-A-LT + HX8678-C-LT
- TOUCH IC : ST1633i (I2C address 0x70)△
- ANTI-GLARE (AG) FILM : AG125H8C25



BONA FIDE TECHNOLOGY LTD CRYSTAL CLEAR TECHNOLOGY SDN BHD				PART NAME <b>MECHANICAL SPECIFICATION</b>		
APPROVED	CHECKED	DRAWN	UNIT : MM NTS	DRAWING NUMBER	SHEET	REV.
		AZHAR 07-05-2018		T500A12C00	1/1	2

Attachment 1

# Product Specification

## TP0119-01

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1.0 INTRODUCTION

The purpose of this specification is defined the general provision and quality requirement apply to 5.0 inch Capacitive Touch module integrated by Crystal Clear Technology. This document, together with the module drawing, is the highest level specification for this product. When users touch module by finger, the module can send coordinates of point at the contact point to host. The finger position information is sent to host by I2C bus which is determined by host through IRQ line.

2.0 GENERAL DESCRIPTION

This document contains the Capacitive Touch module specification. The maximum rating, characteristics, hardware, and inspection of the module are described in the subsequent sections. I2C protocol will be introduced in detail.

2.1 Touch sensor characteristics

- Technology: Use the character of capacitive among the touch electrodes on touch panel to identify the positions of touch signals
- Touch method: Ten fingers multi touch with pressure sensing
- Interface: I2C

2.2 General Specification

Item	Specification	Unit
Screen Diagonal	5.0	inch
Module Outline	121.0(H) x 75.8(V) x 1.1(T) (Excluded FPC)	mm
Touch Area	108.0(H) x 64.8(V)	mm
Controller	ST1633i	-
Cover Lens Material	None	-
Transparency	85	%

3.0 ABSOLUTE MAXIMUM RATING

Absolute Maximum rating of touch panel module is as following

Symbol	Parameter	Value	Unit
VDD	Supply Voltage	-0.3 to +6.0	V
TA	Operating Temperature	-20 to +70	°C
TSTG	Storage Temperature	-30 to +80	°C

Note: If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also, if the module operated with the absolute maximum ratings for a long time, its reliability may drop.



### 4.0 ELECTRICAL CHARACTERISTICS

DC Characteristics (unless otherwise specie, Voltage referenced to Ground, T<sub>A</sub> = -20°C to 70°C

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
VDD	Power Supply		2.7	-	3.6	V
INML	Operating Current		-	16.1	24.0	mA
IIDLE	Idle Current		-	8.1	12.2	mA
IPD	Power Down Current		-	-	20.0	uA
VIH	Logic High Input Voltage	VDD=3.3V	0.85VDD	-	-	V
VIL	Logic Low Input Voltage		-	-	0.15VDD	V
RPU	Input Pull Up Resistor		50	-	60	KΩ
IDRV	Output Driving Current	VOH=0.8VDD	6	-	-	mA
ISINK	Output Sinking Current	VOL=0.2VDD	10	-	-	mA
VLVR	Low Voltage Reset		-	-	2.3	V

### 5.0 PIN DEFINITION

NO.	SYMBOL	I/O	FUNCTION
1	RST	I	Sensor system global reset
2	VDD	P	Power supply
3	GND	P	Ground
4	IRQ	O	Sensor data ready request
5	SDA	I/O	I2C serial data
6	SCL	I	I2C serial clock

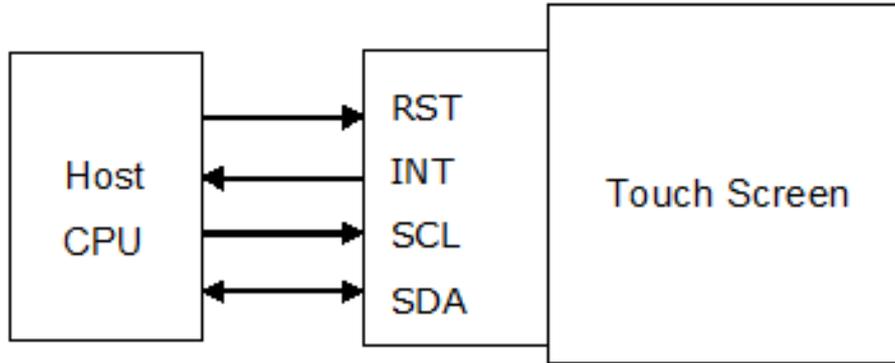
### 6.0 I2C INTERFACE

Touch panel is used as I2C Slave Device, I2C Slave address is 0x70

#### 6.1 Interface Diagram

The system block diagram is as shown in below. There are three communication pins connected between CPU and Touch Panel Module which are including external interrupt INT, I2C pins SCL and SDA. The INT is active low while the touch state is calculated by Touch Panel Module and the touch information can be translated via I2C communication interface.

### Touch Panel Module



### 6.2 Timing Characteristic

Conditions:

VDD - GND = 3.3V

TA = 25°C

Symbol	Parameter	Min	Typ	Max	Unit
f <sub>SCL</sub>	SCL clock frequency	0	-	400	kHz
t <sub>LOW</sub>	Low period of the SCL clock	1.3	-	-	us
t <sub>HIGH</sub>	High period of the SCL clock	0.6	-	-	us
t <sub>f</sub>	Signal falling time	-	-	300	ns
t <sub>r</sub>	Signal rising time	-	-	300	ns
t <sub>SU_STA</sub>	Set up time for a repeated START condition	0.6	-	-	us
T <sub>HD_STA</sub>	Hold time (repeated) START condition. After this period, the first clock pulse is generated	0.6	-	-	us
t <sub>SU_DAT</sub>	Data set up time	100	-	-	ns
t <sub>HD_DAT</sub>	Data hold time	0	-	0.9	us
t <sub>SU_STO</sub>	Set up time for STOP condition	0.6	-	-	us
t <sub>BUF</sub>	Bus free time between a STOP and START condition	1.3	-	-	us
C <sub>b</sub>	Capacitive load for each bus line	-	-	400	pF

